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Sinopsis

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Digital Signal Processing has undergone enormous growth in usage/implementation in the last 20 years and many engineering schools are now offering real-time DSP courses in their undergraduate curricula. Our everyday lives involve the use of DSP systems in things such as cell phones and high-speed modems; Texas Instruments has introduced the TMS320C6000 DSP processor family to meet the high performance demands of today's signal processing applications.

This book provides the know-how for the implementation and optimization of computationally intensive signal processing algorithms on the Texas Instruments family of TMS320C6000 DSP processors. It is organized in such a way that it can be used as the textbook for DSP lab courses offered at many engineering schools or as a self-study/reference for those familiar with DSP but not this family of processors.

This book provides a restructured, modified, and condensed version of the information in more than twenty TI manuals so that one can learn real-time DSP implementations on the C6000 family in a structured course, within one semester. Each chapter is followed by an appropriate lab exercise to provide the hands-on lab material for implementing appropriate signal processing functions. These labs are included on the accompanying companion website to take the reader through the entire process of C6X code writing.

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