

Librería
Bonilla y Asociados
desde 1950



Título:

Autor: Rodriguez-Henriquez, F. , Saqib, N. A. , Díaz Pérez, A. , **Precio:** \$1442.22
Koc, C

Editorial:

Año: 2007

Tema:

Edición: 1ª

Sinopsis

ISBN: 9781441941565

This book covers the study of computational methods, computer arithmetic algorithms, and design improvement techniques needed to implement efficient cryptographic algorithms in FPGA reconfigurable hardware platforms. The concepts and techniques reviewed in this book will make special emphasis on the practical aspects of reconfigurable hardware design, explaining the basic mathematics related and giving a comprehensive description of state-of-the-art implementation techniques. The authors show how high-speed cryptographic algorithms implementations can be achieved on reconfigurable hardware devices without posing prohibited high requirements for hardware resources. The material in this book will be of interest to engineering professionals, programmers, hardware designers, and graduate students interested in the development of security and cryptographic mechanisms at a beginning/intermediate level.